

RD — tells the ULA when the CPU is reading from a device. If the device is the ULA, data will be output onto the databus for the CPU.
 MREQ — this input tells the ULA that the address bus now contains a valid address for a read or write operation. This signal is required to distinguish between a memory or I/O operation.
 INT — interrupt to the CPU operates 50 times per second. Upon receiving this interrupt, the CPU increments a 2 byte counter in memory and scans the keyboard to see if any keys have been pressed.

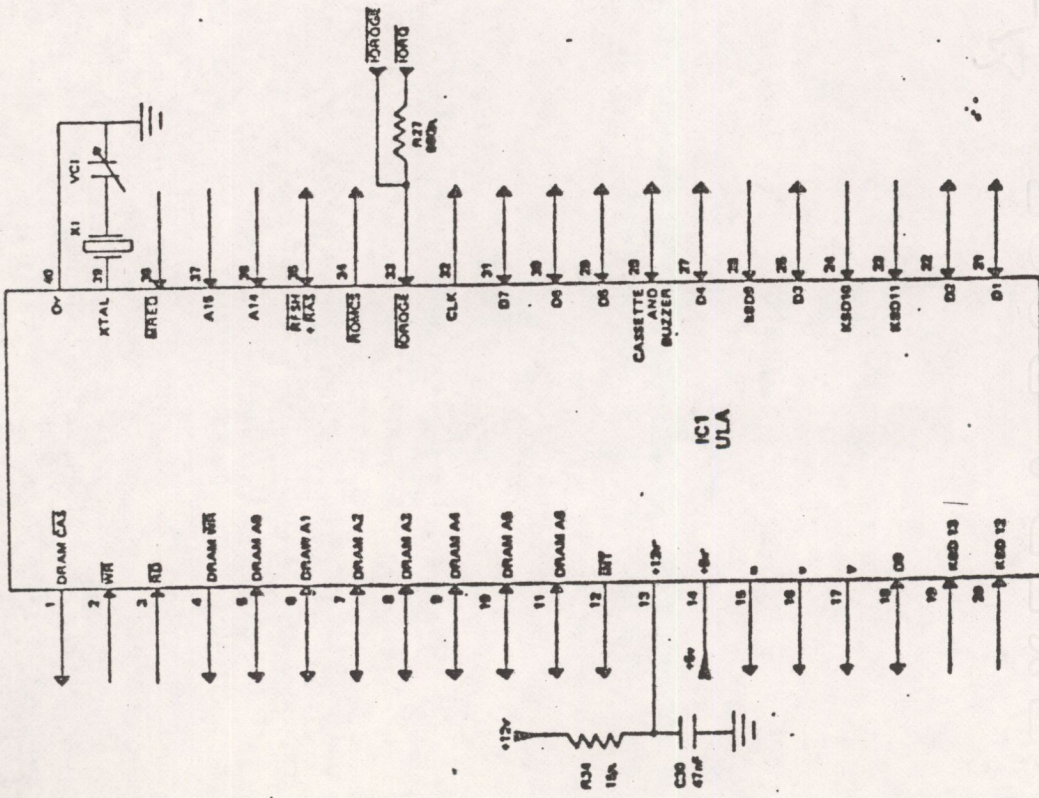


FIG 7a - THE UNCOMMITTED LOGIC ARRAY PIN CONNECTIONS